

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

Claims 1-28 (Cancelled)

29. (New) An IC card comprising:

a card substrate;

a semiconductor chip mounted on the card substrate;

a resin sealing the semiconductor chip;

a plurality of connector terminals formed on the card substrate; and

a casing covering a part of the card substrate;

wherein said IC card has a plurality of modes in which numbers of data terminals used by each mode are different from each other; and

wherein said card substrate has a plurality of terminals which are not covered with said resin and are covered with said casing.

30. (New) An IC card comprising:

a card substrate;

a first semiconductor chip and a second semiconductor chip mounted on the card substrate;

a resin sealing the first semiconductor chip and the second semiconductor chip;

a plurality of connector terminals including data terminals formed on the card substrate; and

a casing covering a part of the card substrate;

wherein said connector terminals are exposed from the casing;

wherein said IC card has a first mode and a second mode in which numbers of the data terminals used by each mode are different from one another; and

wherein said card substrate has a further plurality of terminals which connect said first semiconductor chip and said second semiconductor chip, that are not covered with said resin and that are covered with said casing.

31. (New) The IC card according to Claim 30, wherein said further plurality of terminals are formed on a surface of the card substrate opposite to that on which the connector terminals are formed.

32. (New) The IC card according to Claim 30, wherein said first semiconductor chip is a controller chip

electrically connected to the connector terminals, and said second semiconductor chip is a memory chip electrically connected to the controller chip.

33. (New) The IC card according to claim 32, wherein said controller chip changes the mode in response to the state of a predetermined connector terminal or the state of an input from predetermined connector terminals.

34. (New) The IC card according to Claim 30, wherein said first mode uses one of the data terminals and said second mode uses four of the data terminals to perform four-bit parallel input/output.

35. (New) The IC card according to Claim 34, wherein said IC Card has a third mode, and the third mode uses eight of the data terminals to perform eight-bit parallel input/output.